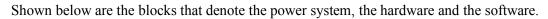
2.1.0. System Architecture



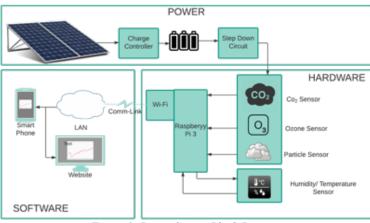


Figure 1: Power System Block Diagram

2.1.1 Hardware Design

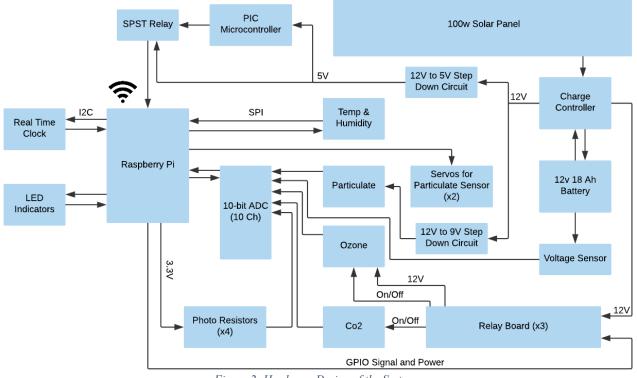


Figure 2: Hardware Design of the System.